

Delayed Cell Count	0	0	100	200	300	300	350	350	400	400	425
Actual Cell Count	0	100	200	300	400	50	150	50	150	25	125
Standby   Active H/w Ctrs after Switchover					450 0	200 0	175 0	25 0			
Time (in minutes)	0	1	2	3	4	5	6	7	8	9	10

Switchover

### Figure 1

CELL COUNTS COLLECTED FROM HERE. SEE FIGURE 3  
FOR DETAILS

All cell busses are 32 bits wide and run at 80Mhz (32 bits \* 80Mhz = 2.5Gbps).

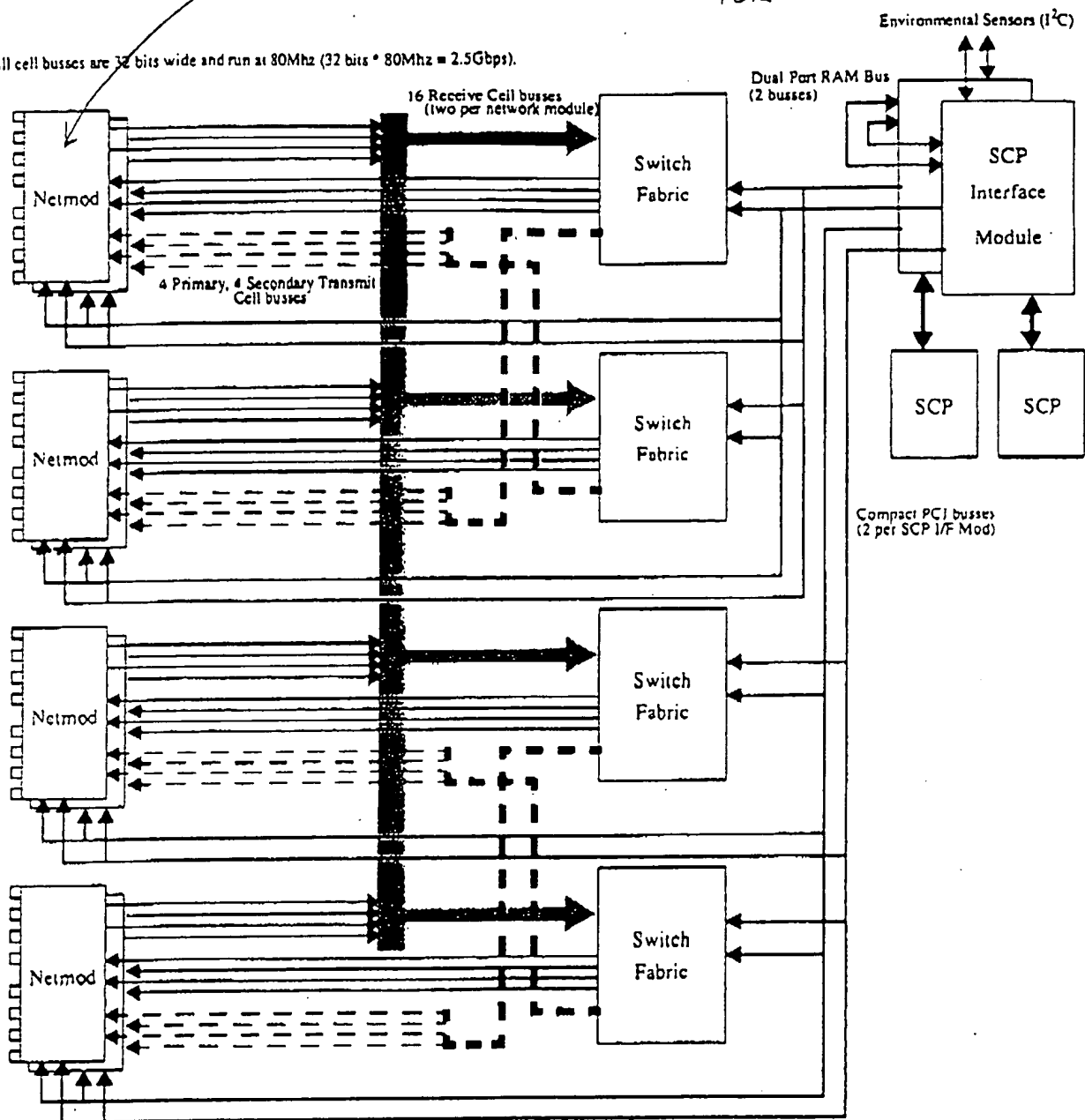
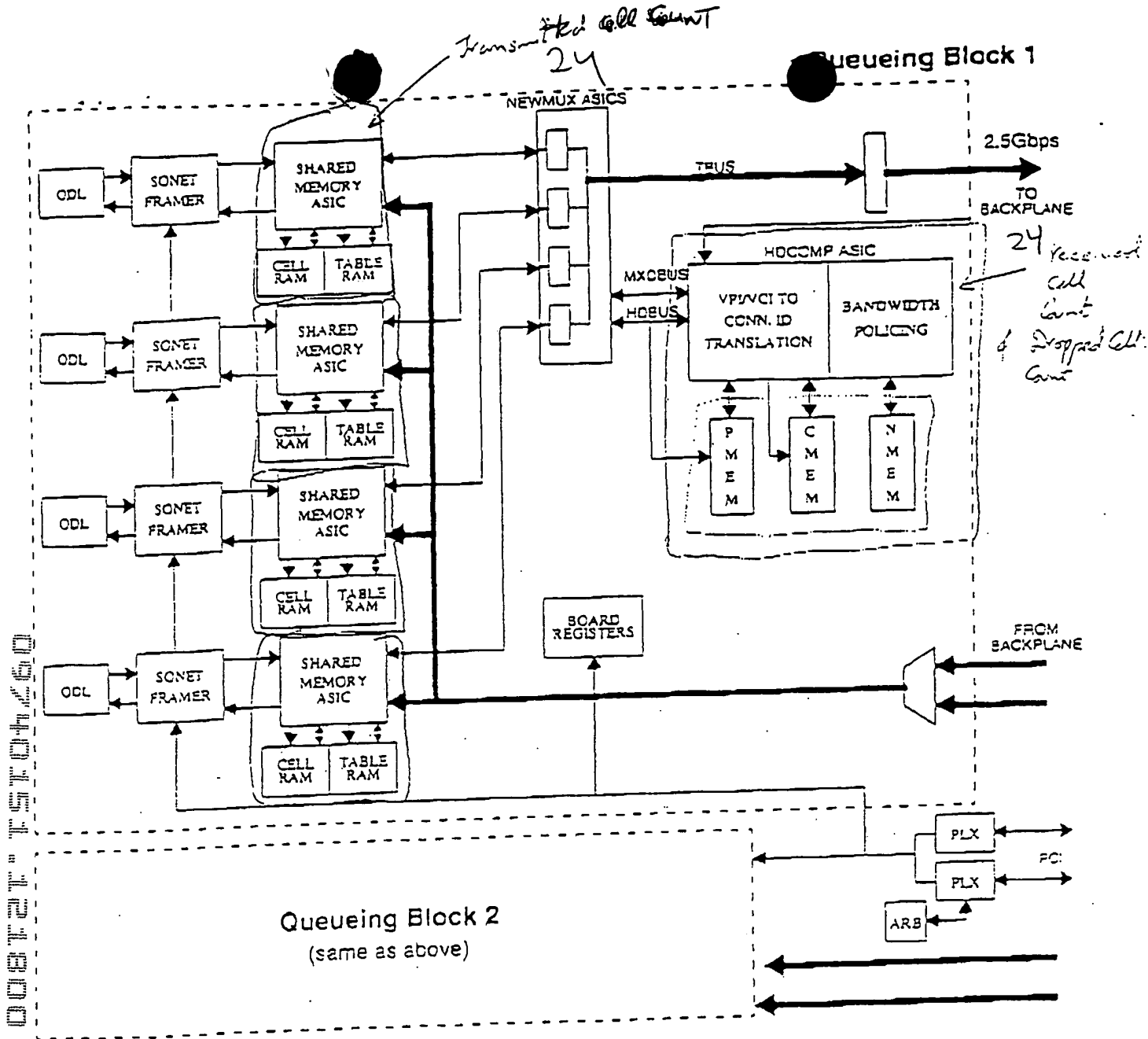


FIG 2

00740151-121800



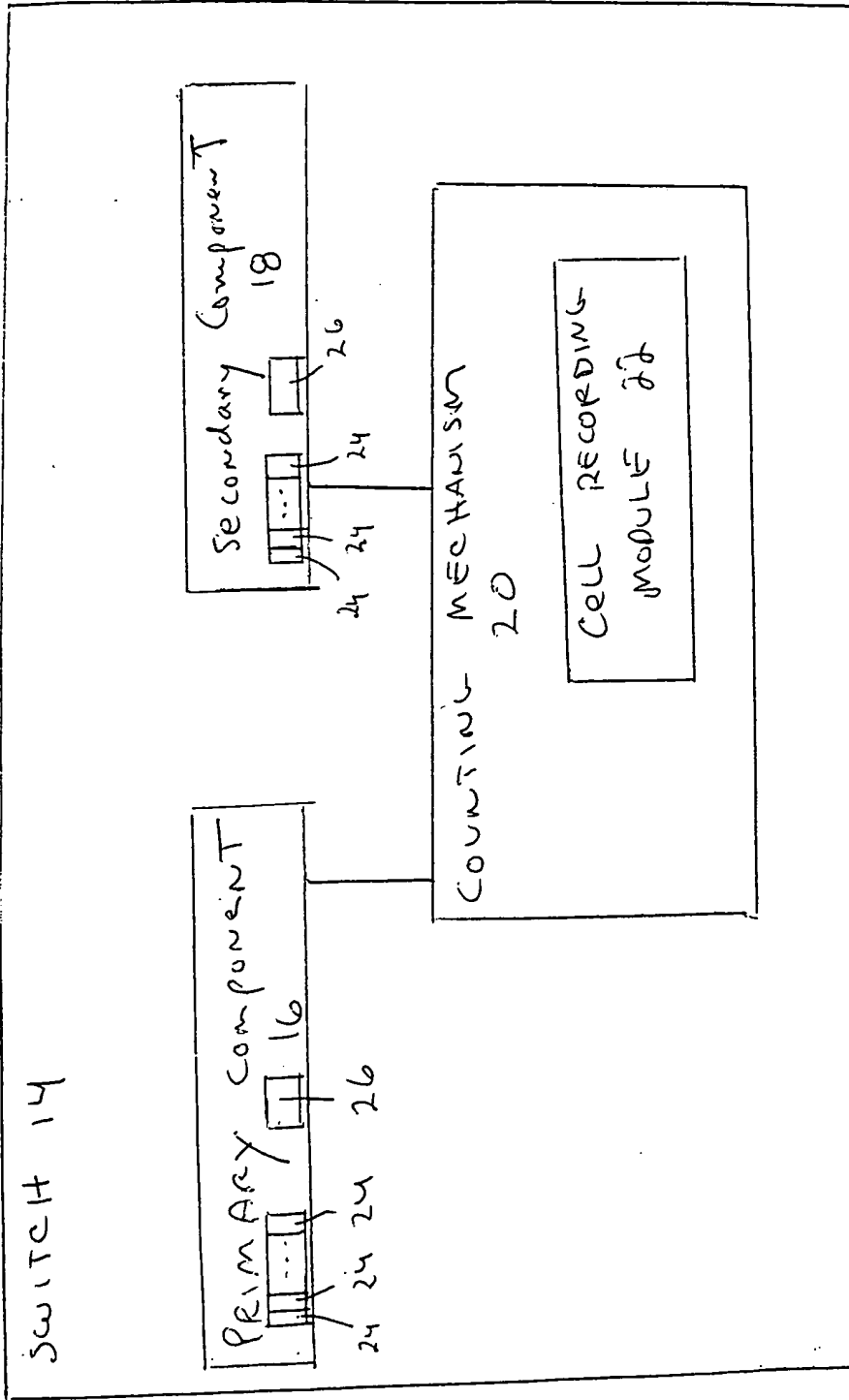


FIG. 4